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(54) **Gas reactions to eliminate contaminants in a CVD chamber**

(57) A multi-step process is used to condition a chemical vapor deposition chamber after cleaning and between successive depositions by removing fluorine residues from the chamber with a hydrogen plasma, and subsequently depositing a solid compound in the chamber to encapsulate any particulates remaining in the chamber.

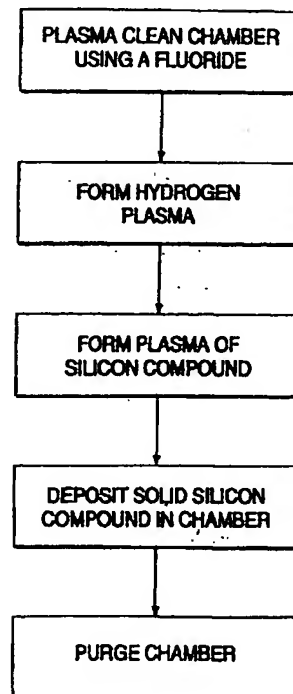


FIG. 2

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Description

[0001] This invention relates broadly to chemical vapor deposition (CVD) processing. More particularly, this invention relates to carrying out reactions with contaminants in CVD chambers to clean the chamber prior to CVD processing.

[0002] CVD is widely used in the semiconductor industry to deposit films of various kinds, such as intrinsic and doped amorphous silicon, silicon oxide, silicon nitride, silicon oxynitride and the like on a substrate. Modern semiconductor CVD processing is generally done in a vacuum chamber by heating precursor gases which dissociate and react to form the desired film. In order to deposit films at low temperatures and relatively high deposition rates, a plasma can be formed from the precursor gases in the chamber during the deposition. Such processes are known as plasma enhanced chemical vapor deposition processes, or PECVD.

[0003] State of the art CVD chambers are made of aluminum and include a support for the substrate to be processed, and a port for entry of the required precursor gases. When a plasma is used, the gas inlet and/or the substrate support will be connected to a source of power, such as an RF power source. A vacuum pump is also connected to the chamber to control the pressure in the chamber and to remove the various gases and particulates generated during the deposition.

[0004] As semiconductor devices on a substrate become smaller and are made closer together, particulates in the chamber must be kept to a minimum. Particulates are formed because during the deposition process, the film being deposited deposits not only on the substrate, but also deposits on the walls and various fixtures, e.g., shields, substrate support and the like in the chamber. During subsequent depositions, the film on the walls etc. can crack or peel, causing contaminant particles to fall onto the substrate. This causes problems and damage to particular devices on the substrate. When individual devices or die are cut from a silicon wafer, for example, damaged devices, such as transistors, can be discarded.

[0005] Similarly, when large glass substrates are processed to form thin film transistors for use as computer screens and the like, up to one million transistors are formed on a single substrate. The presence of contaminants in the processing chamber is also serious in this case, since the computer screen and the like will not be operative if damaged by particulates.

[0006] Thus the CVD chamber must be periodically cleaned to remove particulates between depositions. Cleaning is generally done by passing an etch gas, particularly a fluorine-containing gas such as nitrogen trifluoride, into the chamber. A plasma is then initiated from the fluorine-containing gas which reacts with coatings from prior depositions on the chamber walls and fixtures, i.e., coatings of amorphous silicon, silicon oxide, silicon nitride and the like, and any particulates in the

chamber, to form gaseous fluorine-containing products that can be pumped away through the chamber exhaust system. This is generally followed by a nitrogen purge.

[0007] However, it has been found that fluorine residues remain in the chamber after this cleaning step, and, more importantly, particulates are not totally removed from the chamber. The fluorine residues can adversely affect the film quality of subsequently deposited films, particularly of amorphous silicon over silicon nitride films, and the threshold voltage of devices made therefrom is shifted. The presence of particulates can damage devices on the substrate.

[0008] Thus a process was sought that could condition a CVD chamber between deposition steps using a fluorine-containing gas, and then remove any fluorine residues remaining in the chamber, and at the same time ensure that particulates remaining in the chamber do not fall onto the substrate.

[0009] We have found a multi-step conditioning process for a CVD chamber after cleaning of the chamber of unwanted deposits using a fluorine-containing gas, that removes the fluorine residues from the chamber and reduces the number of particles in the chamber that can fall upon a subsequently processed substrate.

[0010] In a preceding cleaning step, fluorine is passed into the chamber where it reacts with unwanted deposits and particles. In a first conditioning step of the invention, a plasma is formed in the chamber from hydrogen, which reacts with any fluorine residues remaining in the chamber. These are swept away. In a second conditioning step, a plasma is formed from a deposition gas mixture, forming a layer of a solid compound on the walls and fixtures of the chamber, effectively encapsulating any particulates remaining on the internal surfaces of the chamber.

[0011] The deposition gas mixture can be silane and optionally added co-reactants, forming a solid silicon compound such as silicon oxide or silicon nitride in the chamber. Alternatively, the deposition gas mixture can be tetraethoxysilane (TEOS) and oxygen, forming a silicon oxide compound. In general, the deposition gas mixture used in the conditioning step uses some of the same gases that are used for depositing the film on a substrate.

[0012] The following drawings are provided by way of example:

Fig. 1 is a cross sectional plan view of a PECVD chamber useful for depositing thin films on a large area glass substrate.

Fig. 2 is a flow chart setting forth the steps of the preferred process of the invention.

[0013] US Patent 5,366,585 to Robertson et al, incorporated herein by reference, discloses a PECVD chamber suitable for processing large area glass plates. Referring now to Figure 1, a vacuum chamber 113 surrounded by a reactor housing 112 includes a hinged-lid.

A gas manifold 132 is situated over and parallel to a susceptor 116 upon which the substrate is mounted during processing. The gas manifold 132 includes a face plate 192 having a plurality of orifices 193 therein for supplying process and purge gases. An RF power supply 128

[0014] A set of ceramic liners 120, 121 and 122 adjacent to the housing 112 insulate the metal walls of the housing 112 so that no arcing occurs between the housing 112 and the susceptor 116 during plasma processing. These ceramic liners 120, 121 and 122 also can withstand fluorine-containing etch cleaning gases. A ceramic annulus 123 is also attached to the face plate 192 to provide electrical insulation for the face plate 192. These ceramic parts also repel plasma and thus aid in confining the processing plasma close to the substrate and aid in reducing the amount of deposit build-up on the walls of the housing 112.

[0015] After the last substrate on which a layer is to be deposited is removed from the chamber, a standard fluorine containing gas clean is first carried out in the chamber in a conventional manner. A flow of 800 sccm of nitrogen trifluoride is established with the gas inlet valve wideopen, producing a pressure of 200 millitorr in the chamber, with a susceptor-gas manifold spacing of 1600 mils. An RF power of 1600 Watts is applied to the gas manifold to produce a plasma. The cleaning plasma is continued for a period of about one minute for each 2000 angstroms of amorphous silicon film that had been previously deposited in the chamber. Cleaning plasma is additionally continued for about one minute for each 4000 angstroms of silicon nitride film previously deposited on the substrate within the chamber.

[0016] The following two step conditioning process is used to remove fluorine residues remaining after the above CVD chamber clean step and to deposit a thin, inactive solid compound film on the walls and fixtures in the chamber to encapsulate particles.

[0017] As an example of the present process, in a first conditioning step, a hydrogen plasma was formed in the chamber by passing 1200 sccm of hydrogen into the chamber for 30 seconds, creating a plasma using 300 Watts of power. The hydrogen plasma reacted with fluorine present in the chamber, forming HF which was readily removable via the chamber exhaust system. The chamber was maintained at the temperature to be used for subsequent deposition, and a pressure of 1.2 Torr. The spacing between the substrate support and the gas manifold was 1462 mils.

[0018] In a second conditioning step, a thin film of silicon nitride was deposited under the same spacing, temperature and pressure conditions, but increasing the power to 800 Watts and changing the gases. The silicon nitride film was deposited by passing 100 sccm of silane, 500 sccm of ammonia and 3500 sccm of nitrogen into the chamber for an additional 30 seconds.

[0019] The total time needed to condition the chamber for subsequent deposition processing is thus only about

one minute. The thin silicon nitride film coats the walls and fixtures of the chamber, thereby encapsulating and sealing any remaining particles in the chamber after the cleaning step so they cannot fall onto the substrate to be processed. The deposited silicon nitride layer also reduces outgassing of wall materials and also further reduces any remaining fluorine-containing materials from the chamber.

[0020] The above two-step conditioning process is employed with standard cleaning processes, including clean stabilization, plasma clean with a fluorine-containing gas, followed by a nitrogen purge. After the hydrogen plasma treatment and the silicon compound deposition of the present conditioning process, the chamber may be purged with nitrogen.

[0021] The above process is preferred because it both removes fluorine-containing residues and reduces the number of particles in the chamber with a minimum reduction in system throughput. Alternative single-step conditioning processes using the same amount of processing time have been tried but are not as effective as the present process. A single 60 second silicon nitride deposition process is effective for reducing particles, but is less effective for reducing fluorine residues. It also creates a thicker wall deposit which must be etched away in a subsequent cleaning step. A single 50 second step of forming a hydrogen plasma is effective for reducing fluorine residues, but is not effective for reducing particles. A single 60 second step of amorphous silicon deposition, formed by adding silane to the hydrogen plasma process, is effective for reducing fluorine residues because of the high hydrogen atom production. However, the particle reduction is not as effective as the silicon nitride deposition. Also, again, the amorphous silicon deposition would need to be removed in a subsequent cleaning step.

[0022] Alternative two-step conditioning processes using the same amount of processing time are not as effective either. A thirty second amorphous silicon deposition plus a 30 second silicon nitride deposition would be effective for reducing fluorine residues and particles. However, the added amorphous silicon wall deposit would need to be removed in a subsequent cleaning step. The above multi-step cleaning and conditioning process is employed with conventional stabilization of the reaction chamber between cleaning steps, and a nitrogen purge can be used after the fluorine cleaning step and after the silicon nitride deposition step, in preparation for subsequent CVD processing.

[0023] Fig. 2 is a flow chart illustrating the preferred sequence of steps of the present CVD chamber cleaning and conditioning processes. The plasma CVD chamber is first cleaned with a fluorine-containing gas, a hydrogen plasma is formed in the chamber to remove fluorine residues, a plasma of a silicon compound precursor gases is formed, a solid silicon compound is deposited on the interior of the chamber, and the chamber finally is purged with an inert gas prior to inserting a sub-

strate into the chamber upon which a material is to be deposited.

[0024] Although the present process has been described in terms of particular embodiments, it will be apparent to one skilled in the art that various changes in the gases, reaction conditions, and the like can be made and are meant to be included herein.

[0025] Further, although a particular CVD chamber has been described herein, many CVD chambers are available commercially and can be cleaned and conditioned in accordance with the present process. The invention is to be limited only by the scope of the appended claims.

Claims

1. A method of cleaning a vapor deposition chamber, the method comprising the steps of:

reacting the contaminants in the chamber with a halogen gas thereby producing a halogen reaction product;
removing the halogen reaction product;
forming a hydrogen plasma in the chamber and allowing the hydrogen to react with any residual halogen gas to produce a hydrogen reaction product;
removing the hydrogen reaction product; and
forming a silane plasma in the chamber and allowing the silane plasma to encapsulate contaminants remaining in the chamber.

2. The method of claim 1, wherein the silane plasma forms a silicon nitride film from a gas mixture comprising about 100 sccm silane, about 500 sccm ammonia, and about 3500 sccm nitrogen gas.
3. The method of claim 1 or claim 2, wherein the silane plasma forms a silicon nitride film by depositing silicon nitride on an interior surface of the chamber for a period of about 30 seconds.
4. The method of any one of claims 1 to 3, wherein the chamber is maintained at a pressure of about 1.2 Torr during deposition of the silicon nitride film.
5. The method of any one of claims 1 to 4, wherein the chamber pressure is about 1.2 Torr and hydrogen gas is introduced into the chamber at a flow rate of about 1200 sccm while the hydrogen plasma is conditioning the interior surface of the chamber.
6. A method of processing a substrate in a processing chamber, the method comprising:

cleaning the chamber with one or more reactive gases;

after the chamber has been cleaned, conditioning the chamber by forming a silicon nitride film on an interior surface of the chamber; and
after the chamber has been conditioned, processing the substrate in the chamber.

7. The method of claim 6, further comprising further conditioning the chamber by forming a second semiconductor film over the interior surface of the chamber.
8. The method of claim 6 or claim 7, wherein cleaning the chamber comprises forming a plasma from a fluorine-containing gas.
9. The method of claim 6 or claim 7, wherein cleaning the chamber comprises forming a plasma from a hydrogen-containing gas.
10. A method of vapor deposition, the method comprising the steps of:
 - (a) reacting contaminants in a vapor deposition chamber with a halogen gas thereby producing a halogen reaction product;
 - (b) removing the halogen reaction product from the chamber;
 - (c) forming a hydrogen plasma in the chamber and allowing the hydrogen plasma to react with any residual halogen gas to produce a hydrogen reaction product;
 - (d) removing the hydrogen reaction product from the chamber;
 - (e) forming a silane plasma in the chamber and allowing the silane plasma to coat contaminants remaining in the chamber; and
 - (f) depositing a film on a substrate surface in the chamber by vapor deposition.
11. The method of claim 10, further comprising:
 - (g) removing the substrate surface from the chamber; and
 - (h) repeating steps (a)-(g) a plurality of times.
12. The method of claim 10 or claim 11, wherein the halogen gas is or comprises fluorine.

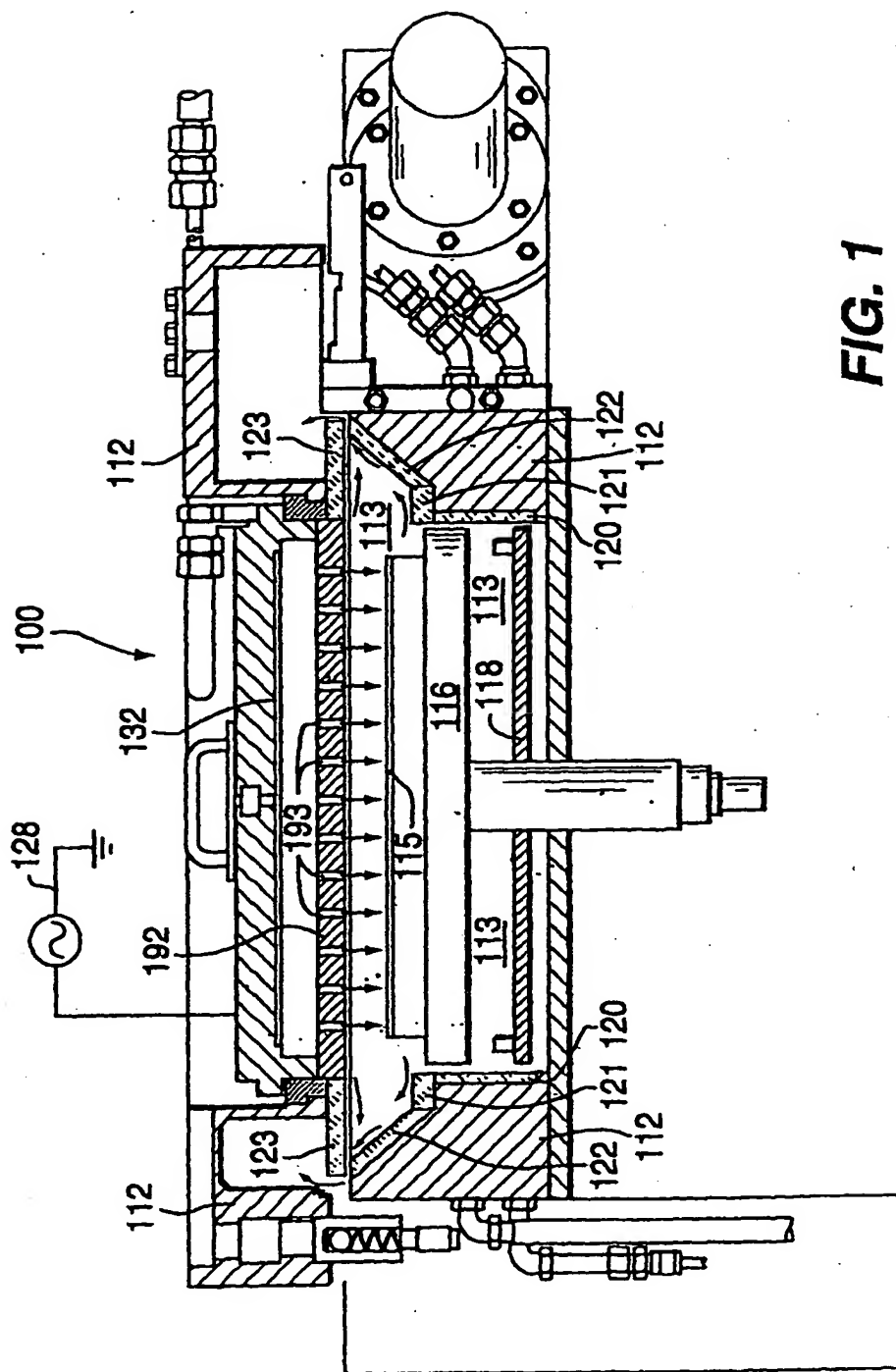


FIG. 1

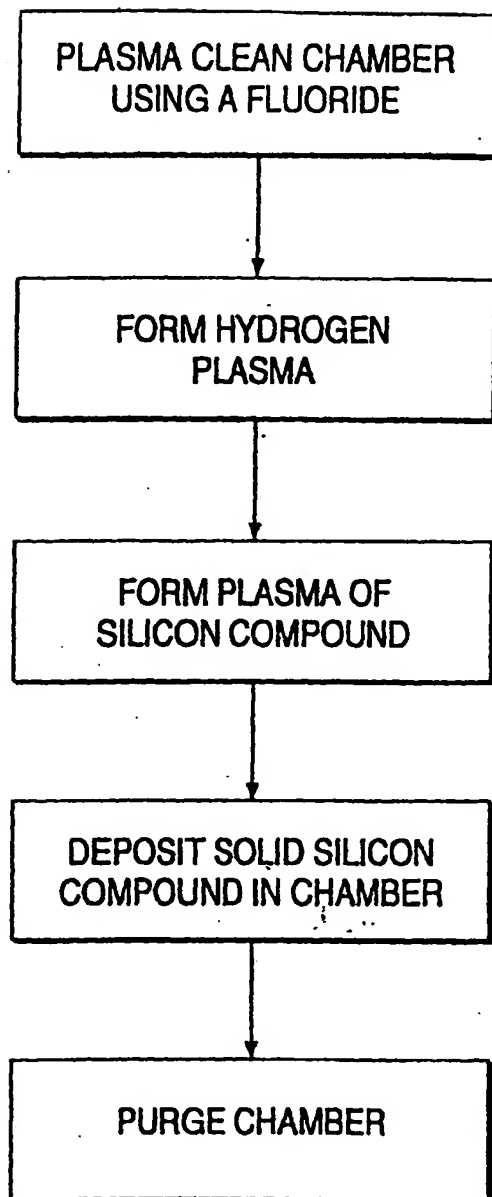


FIG. 2



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EUROPEAN SEARCH REPORT

Application Number
EP 01 30 4237

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Place of search MUNICH		Date of completion of the search 28 August 2001	Examiner De Anna, P
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons a : member of the same patent family, corresponding document</p>			

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**ANNEX TO THE EUROPEAN SEARCH REPORT
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